

Interconnect Fabric Module

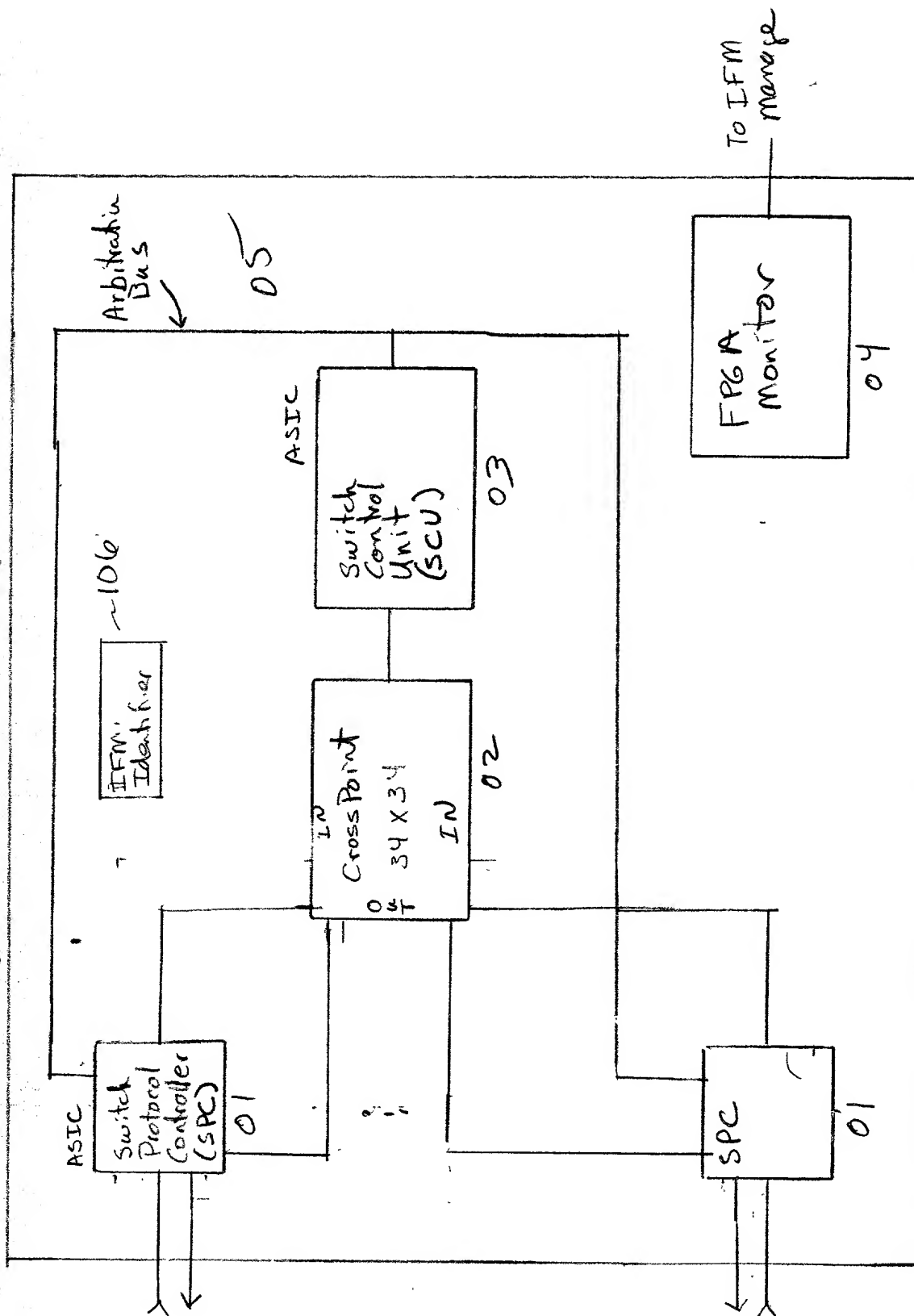


Fig 1

Switch Protocol Controller (SPC)

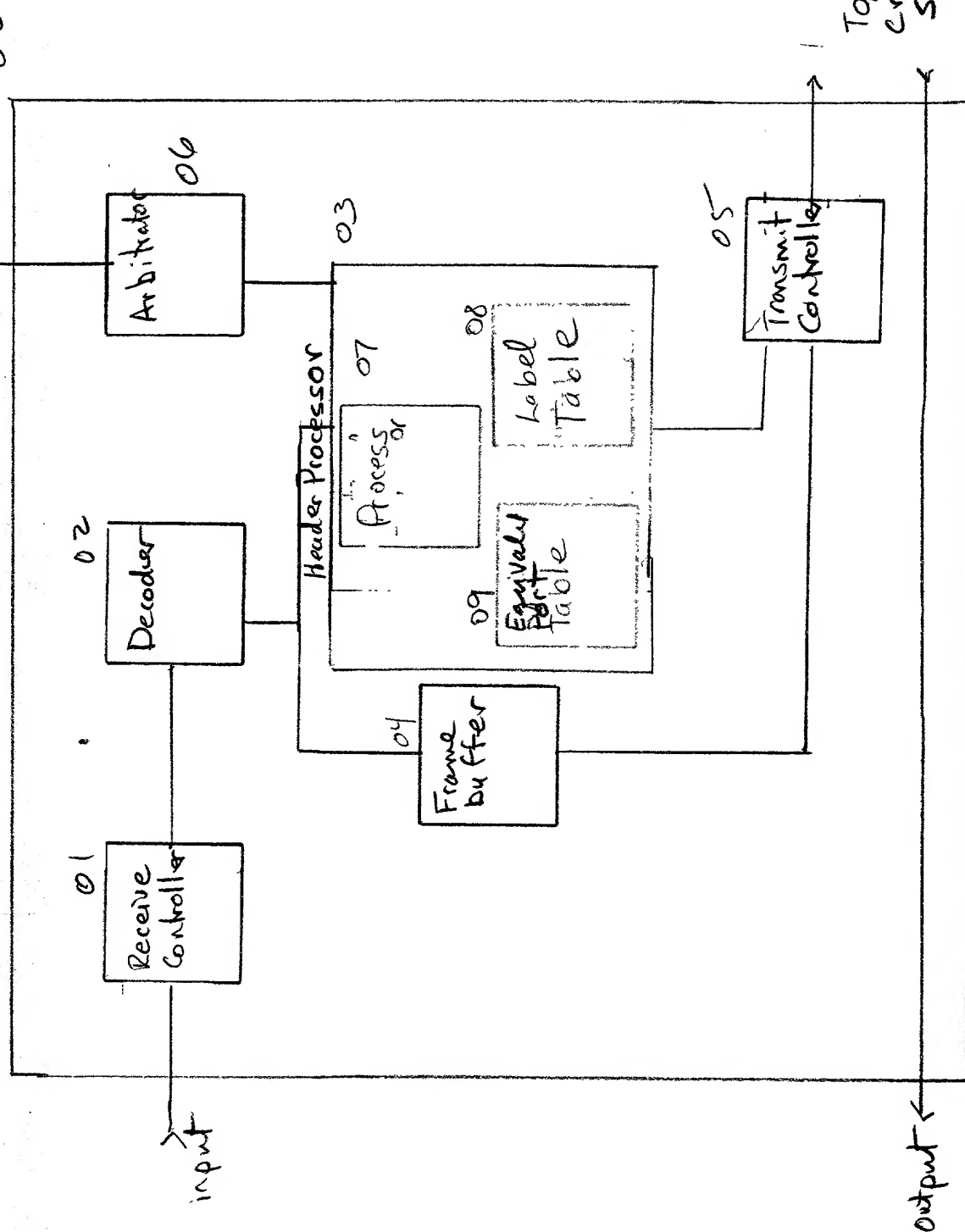


Fig 2

Label Table

[illegible]

M
∞
L

Frame

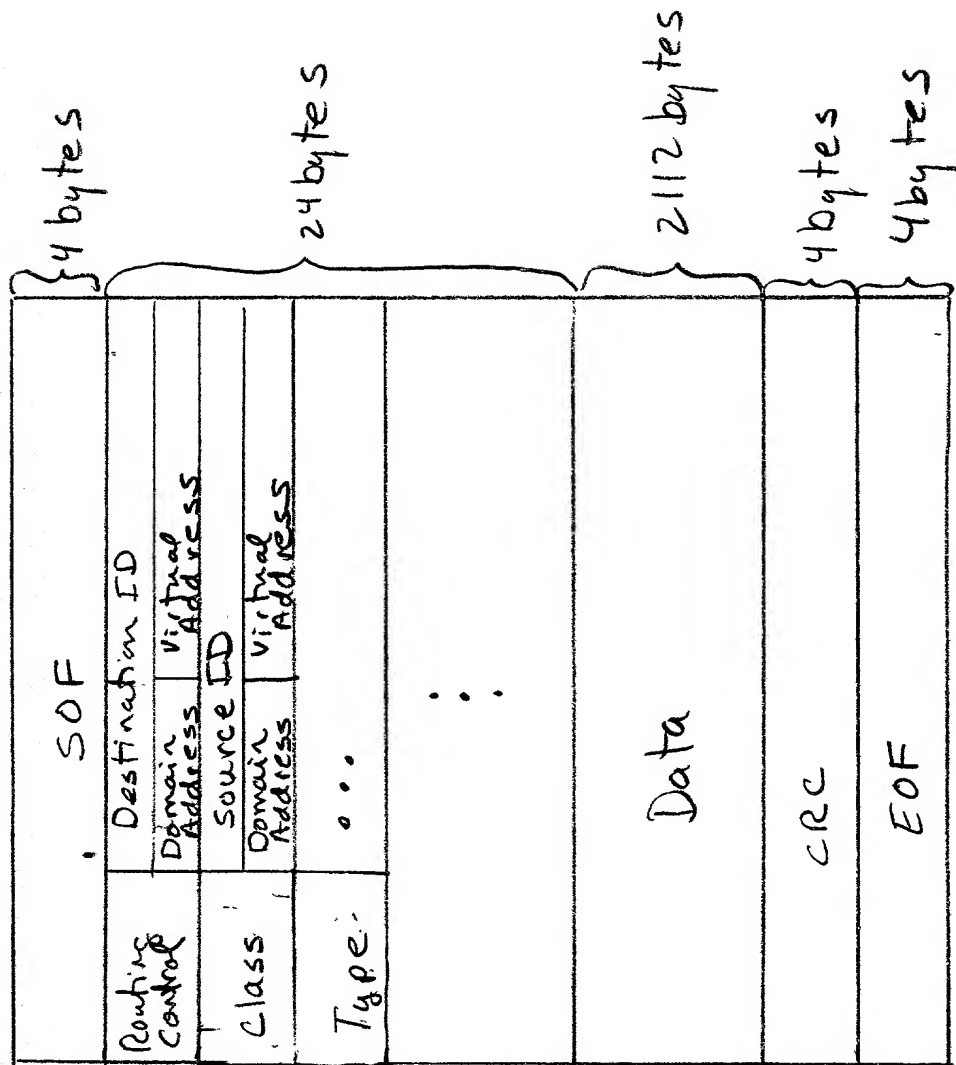


Fig 4

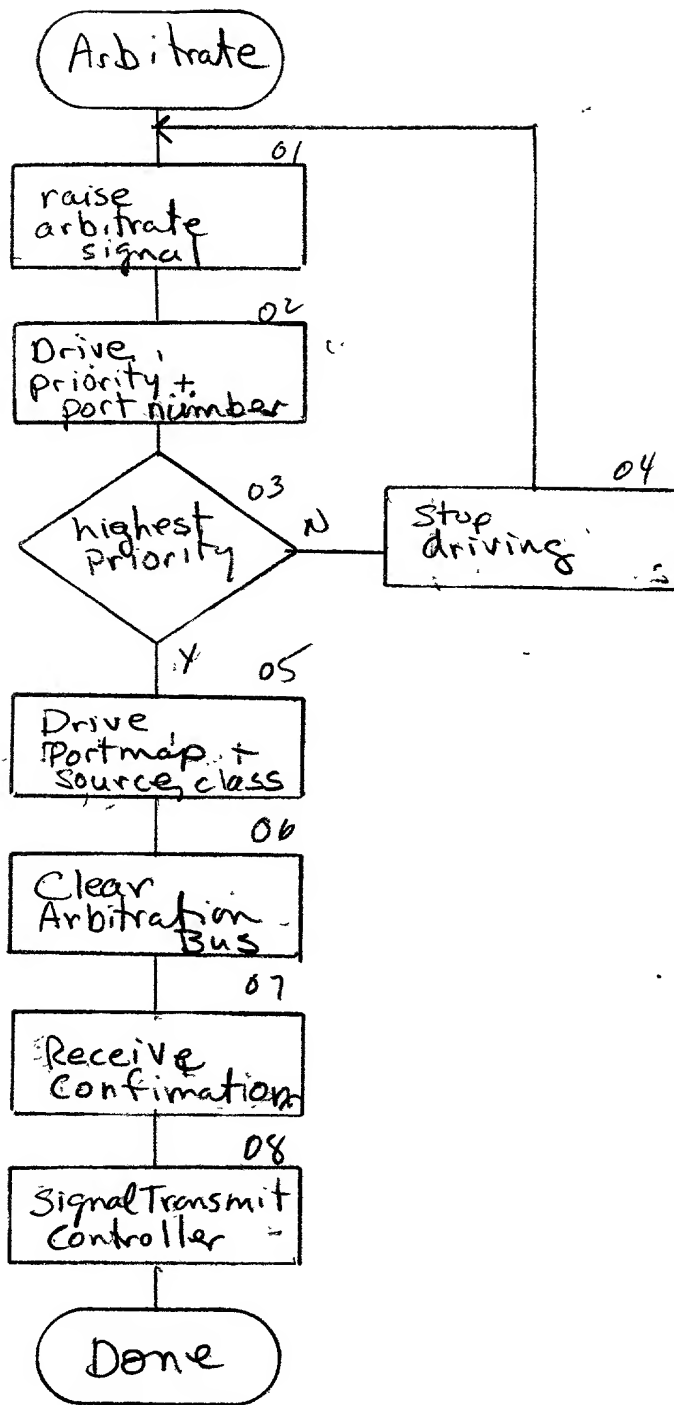


Fig 5

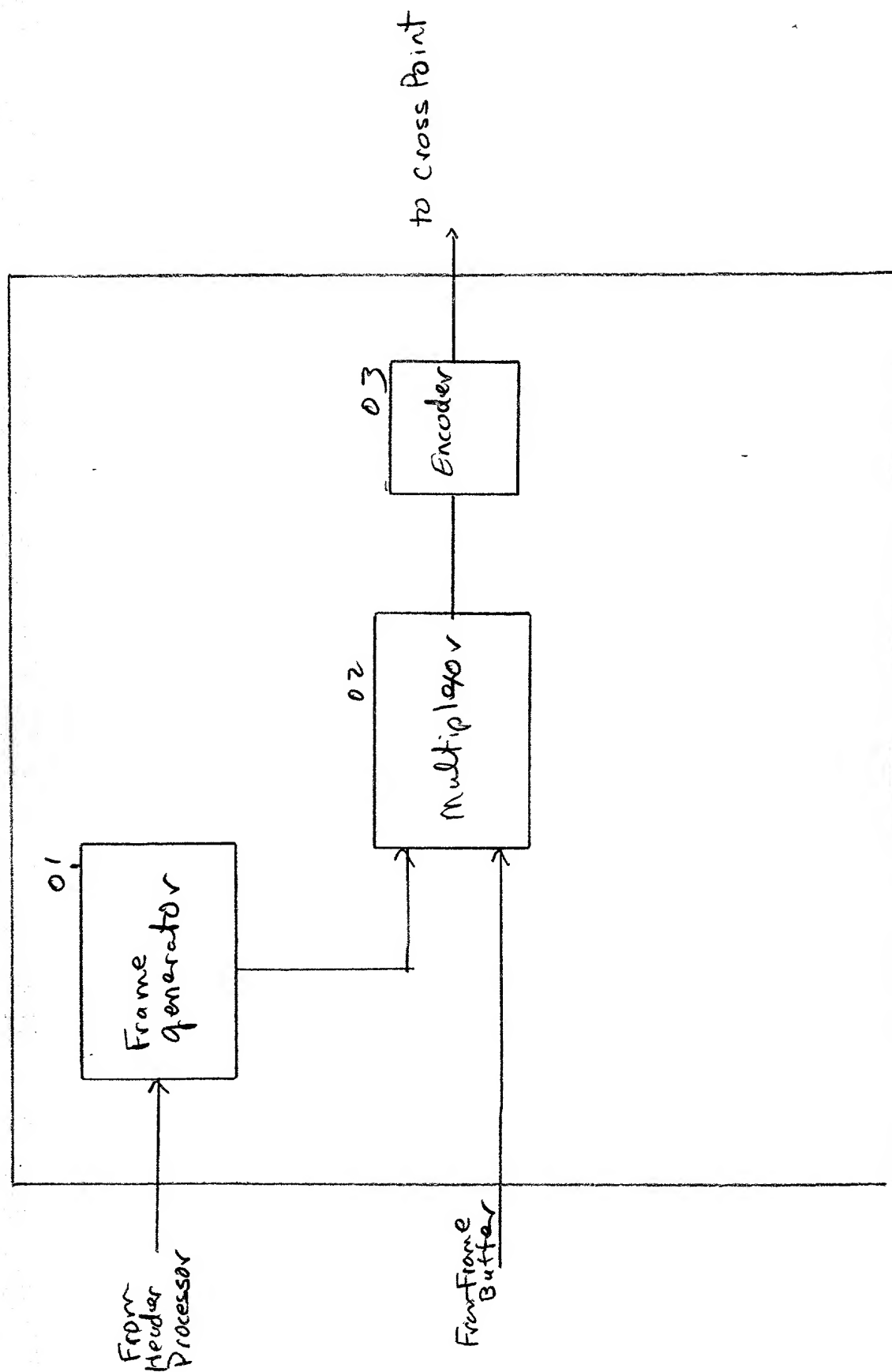


Fig 6

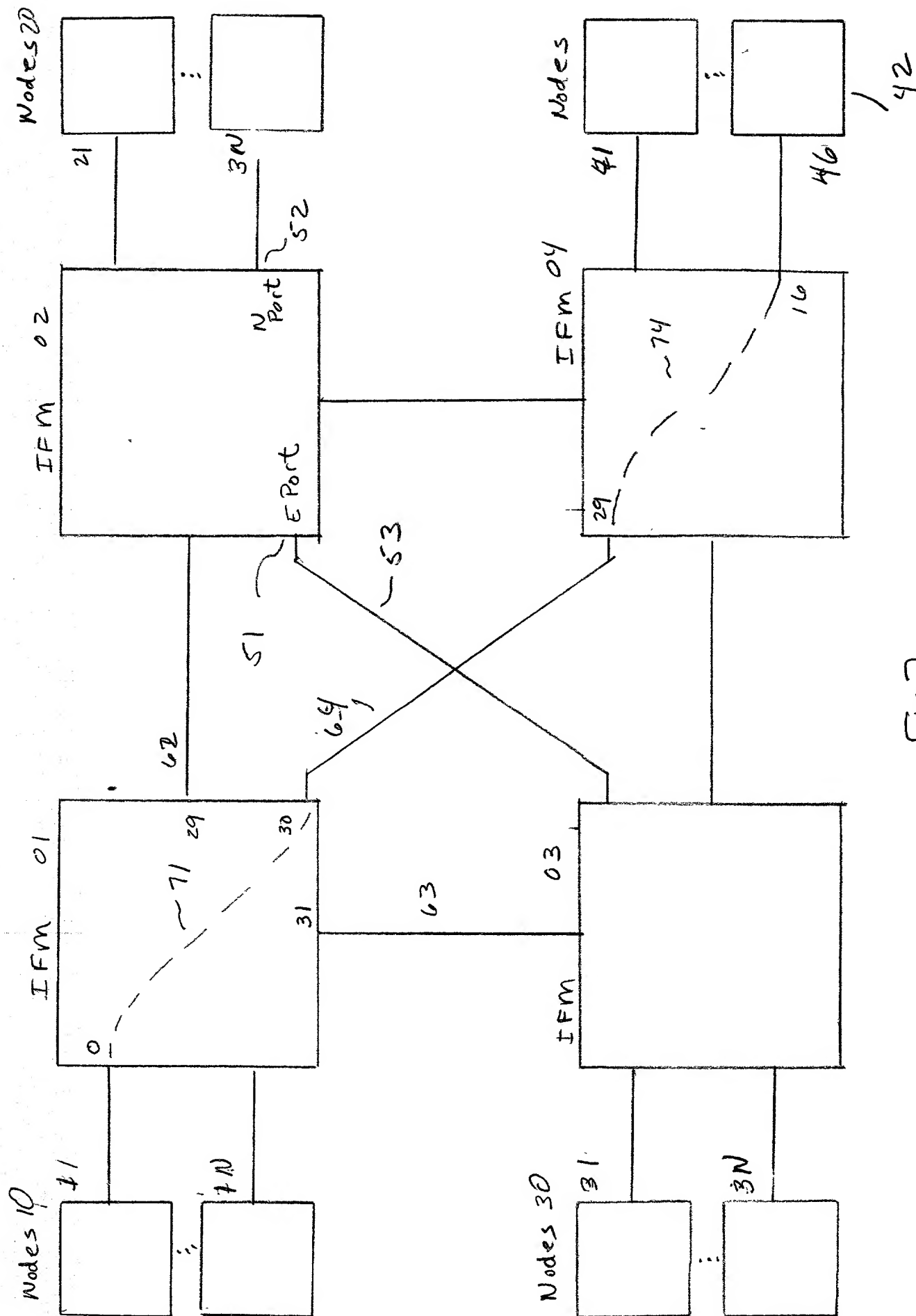


Fig 7

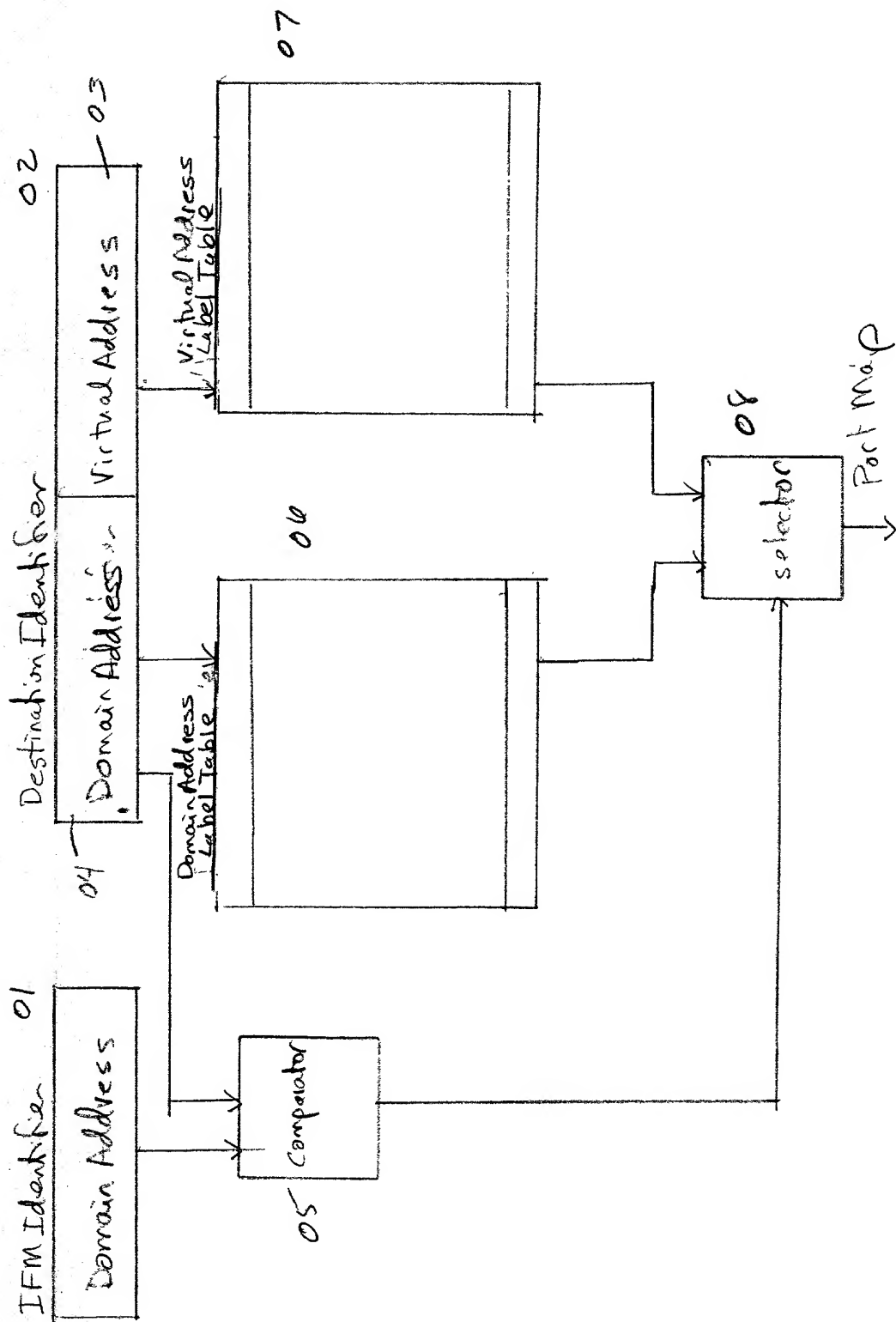


Fig. 8

Quad Switch Protocol Controller

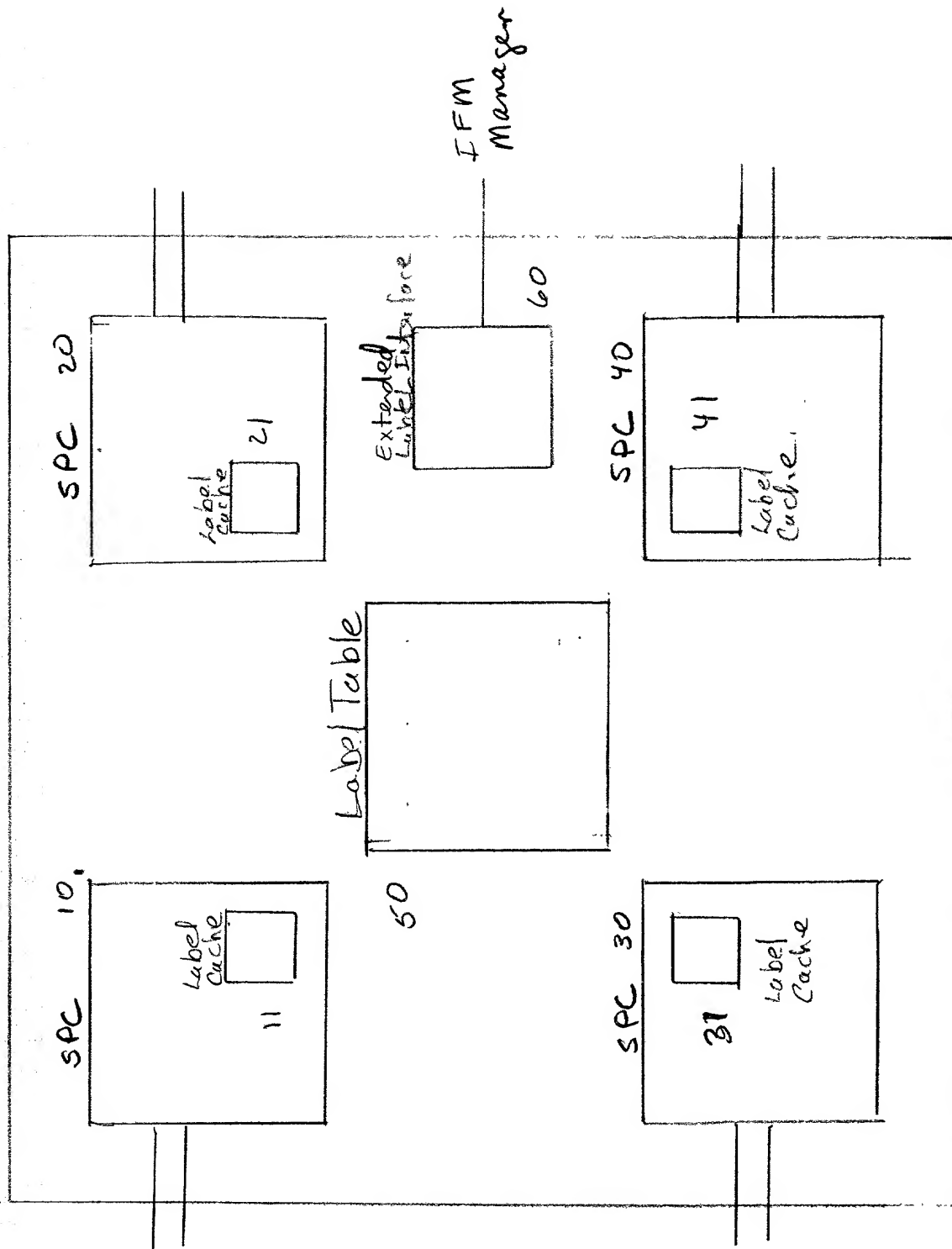


Fig. 9

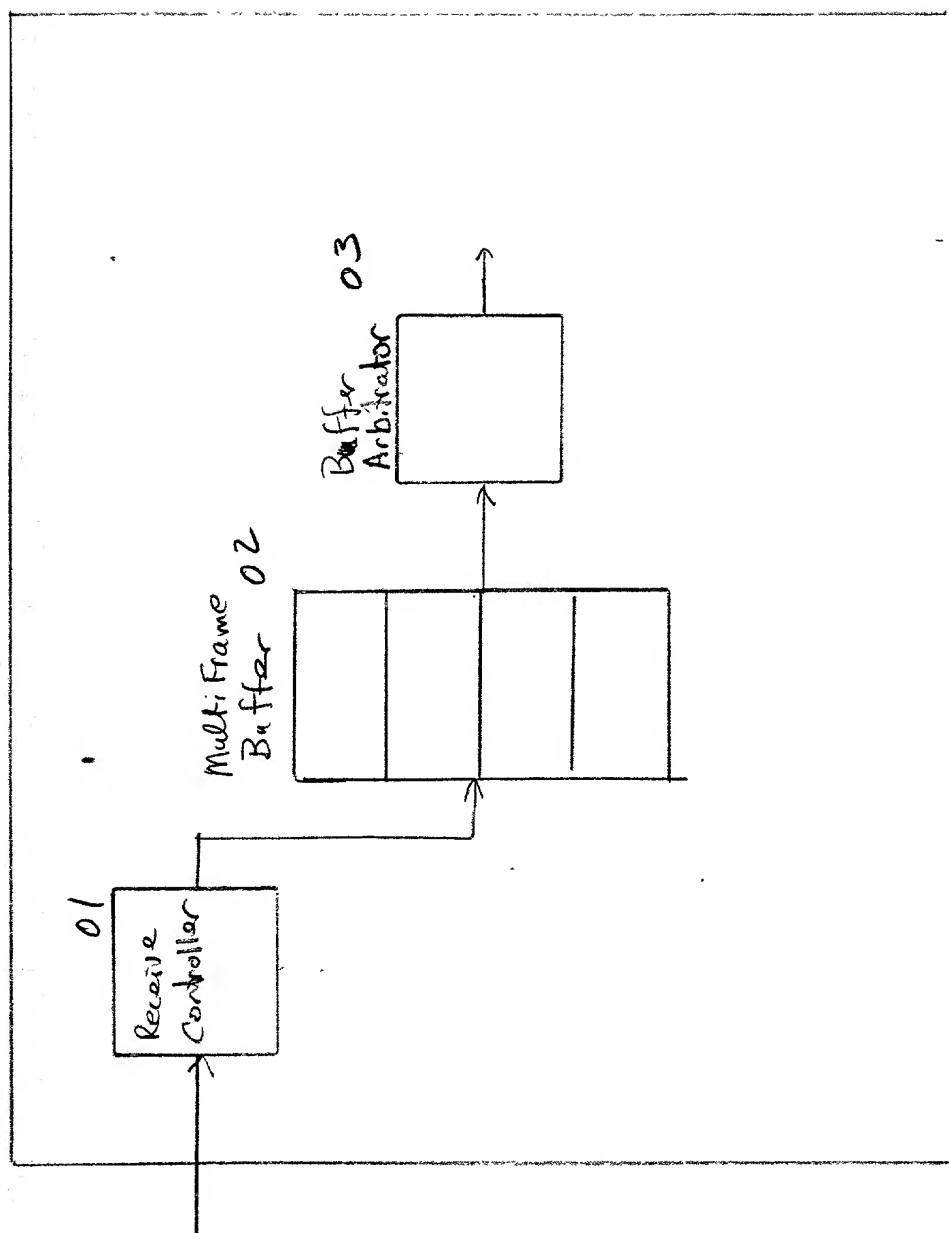


FIG 10

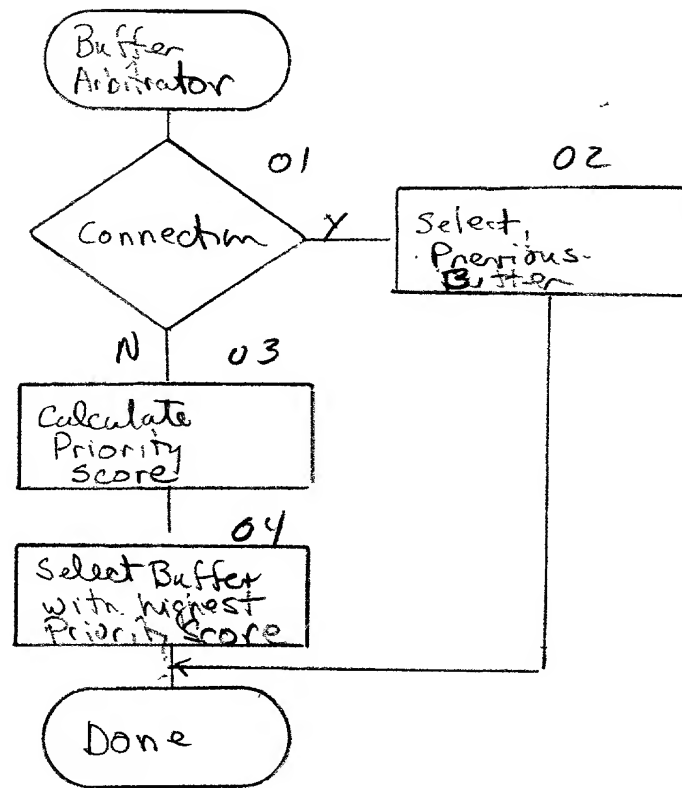


Fig 11

FIGURE 12

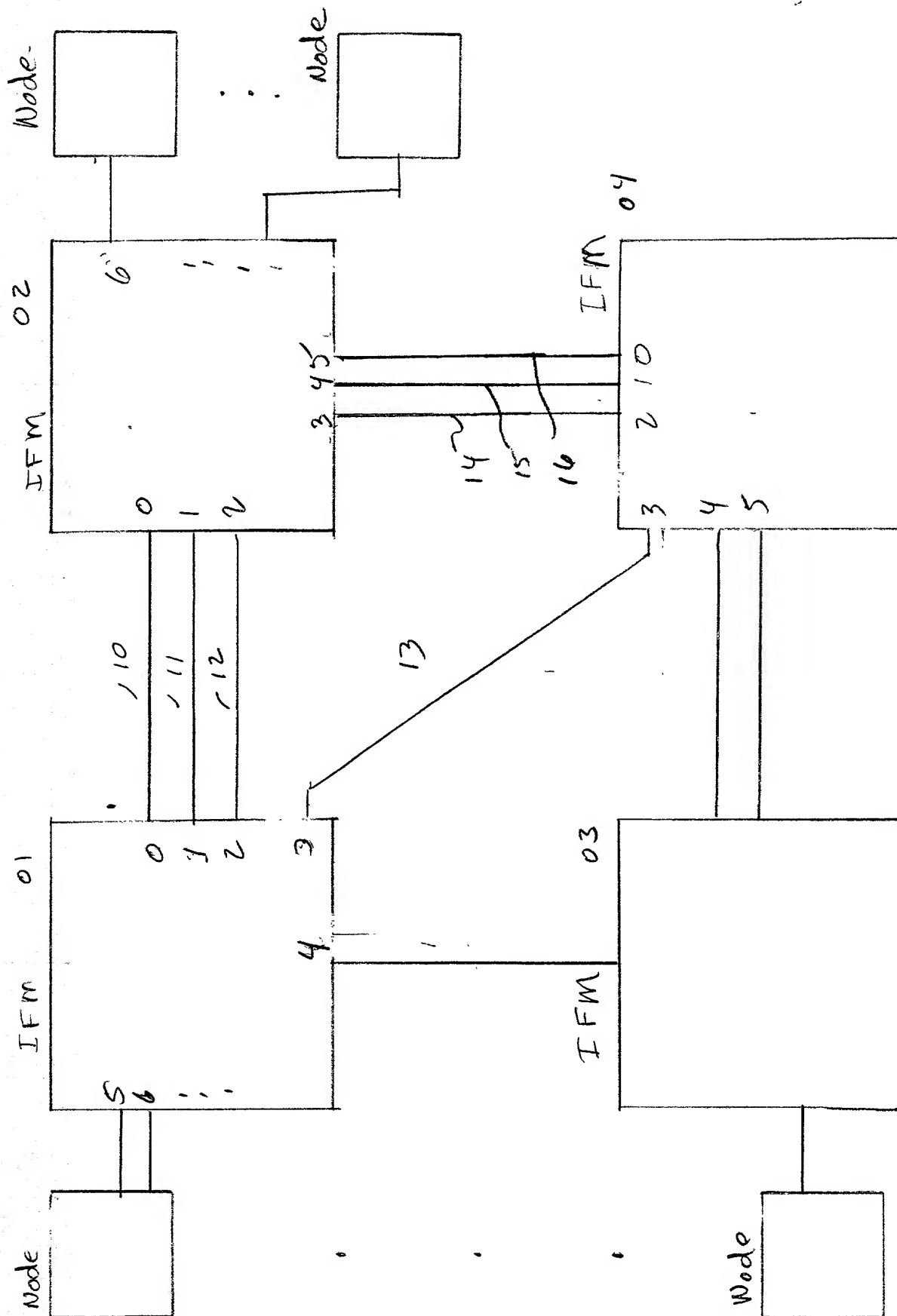


Fig 12

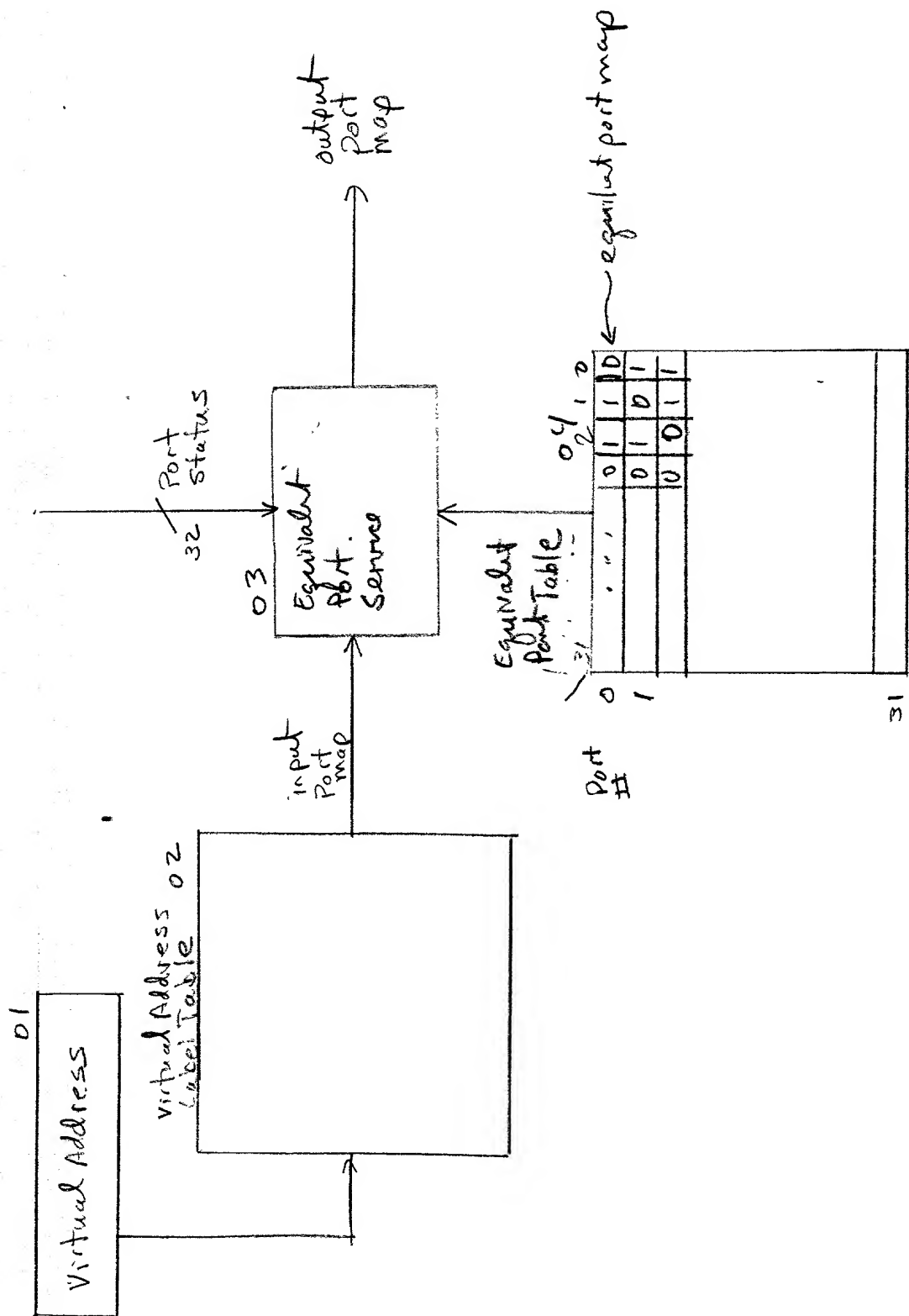


Fig 13

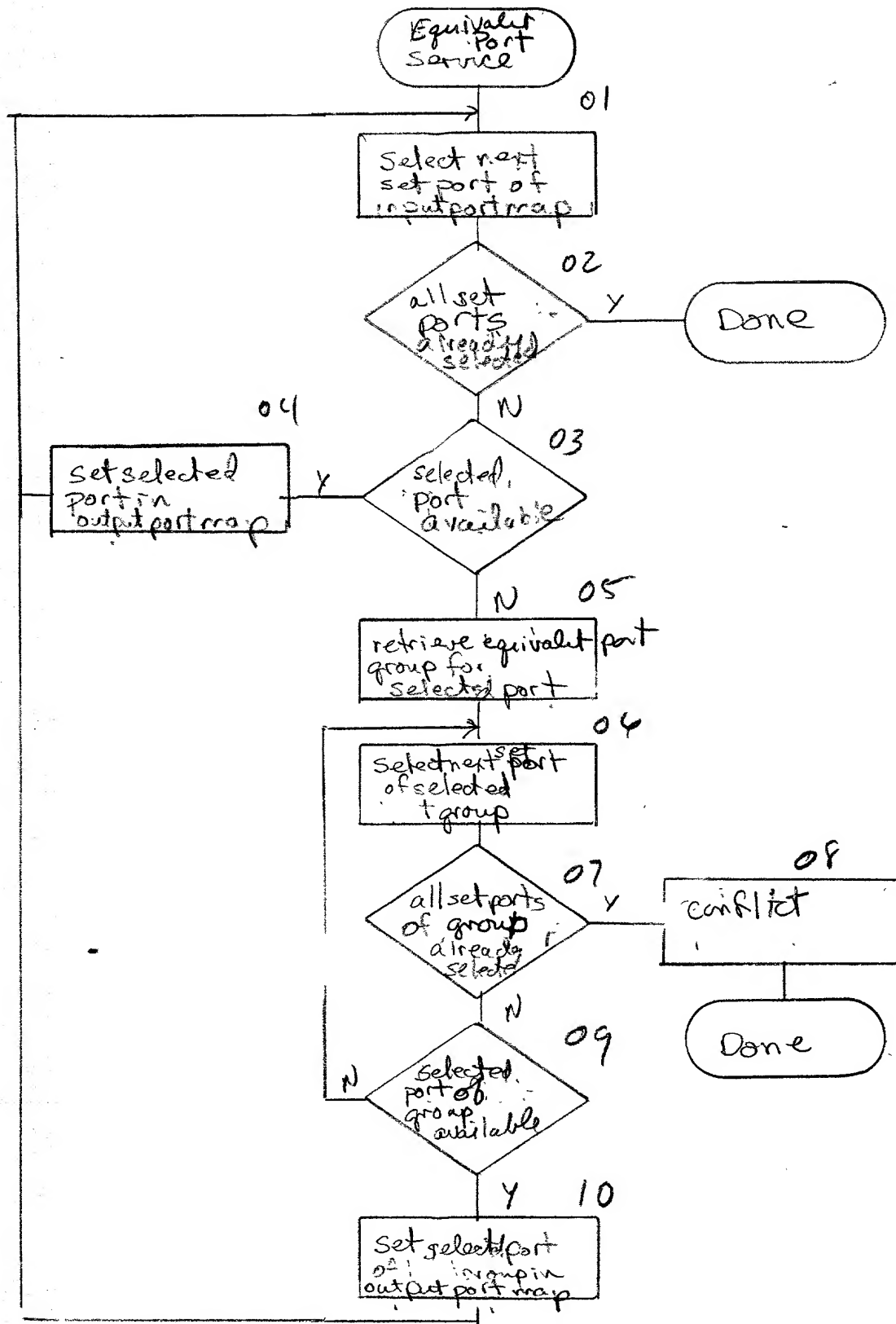


Fig 14

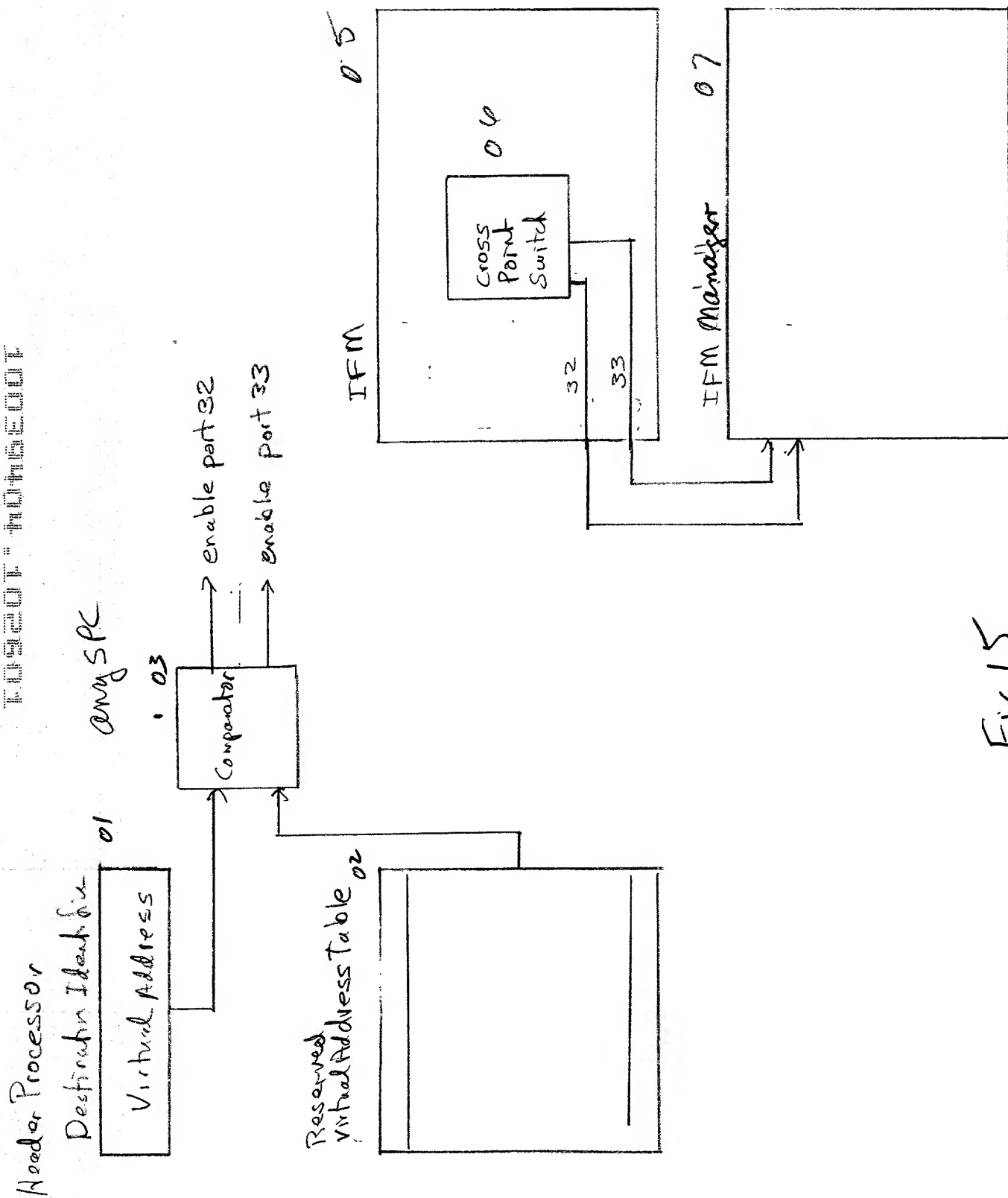
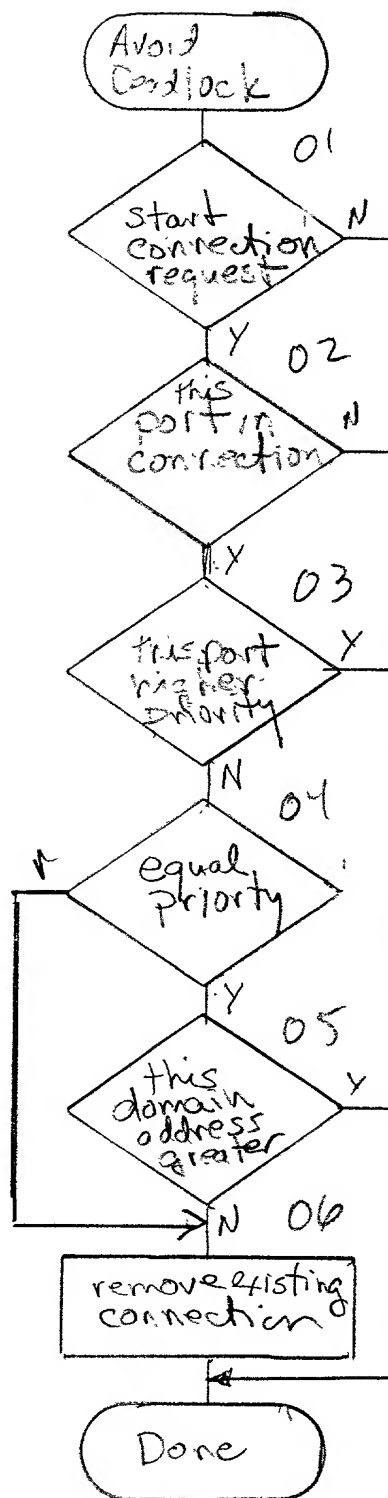


Fig 15

10022007-101000



Under end-to-end
not established

Fig 17

TESTING THE OUT

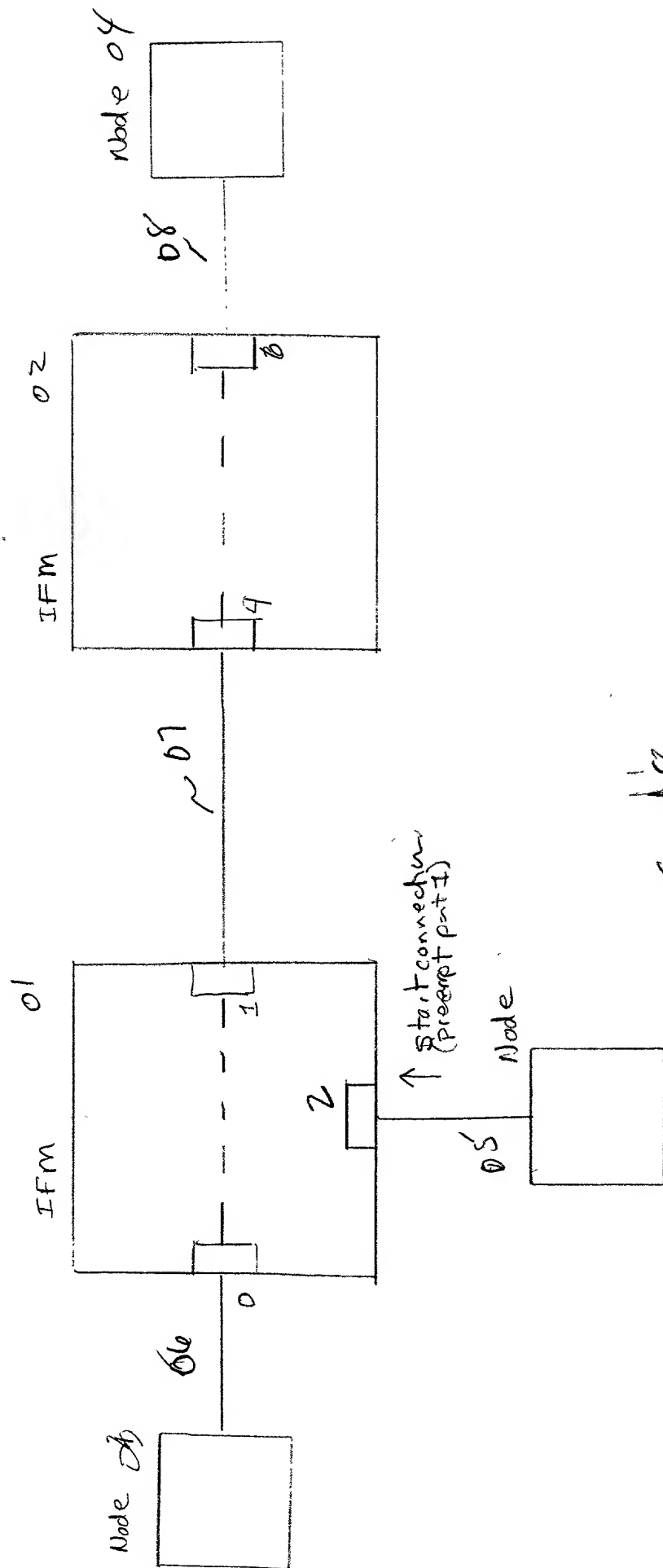


Fig 18

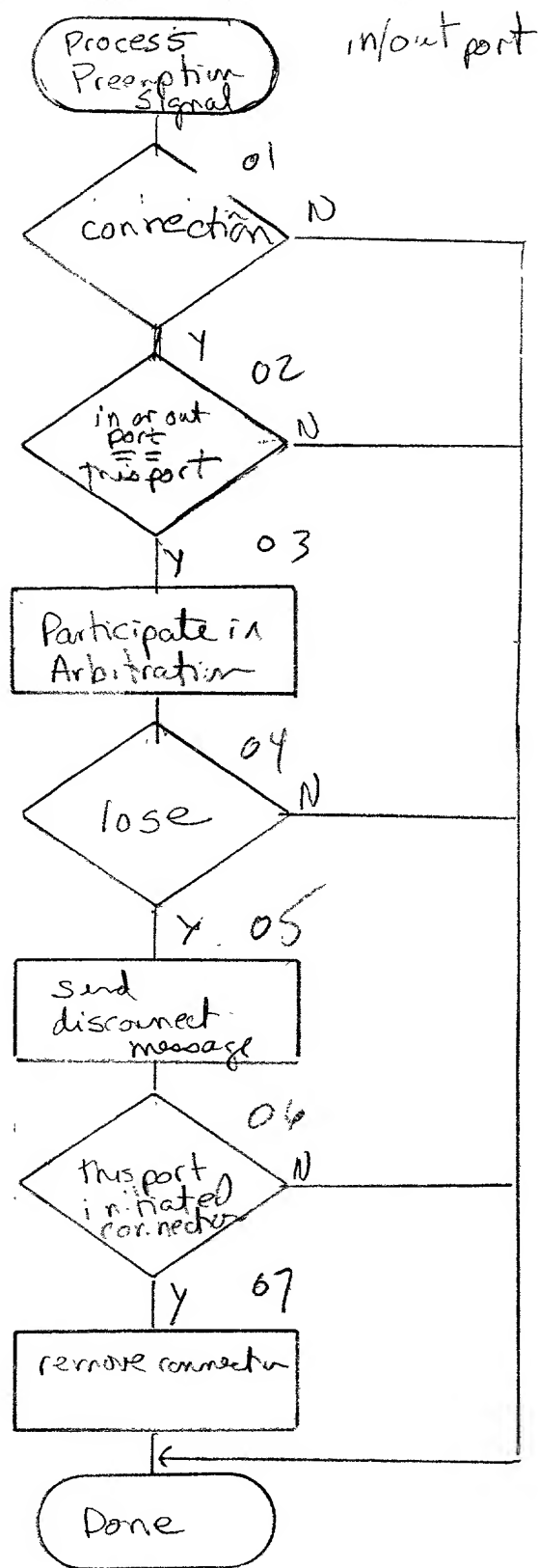


Fig. 19

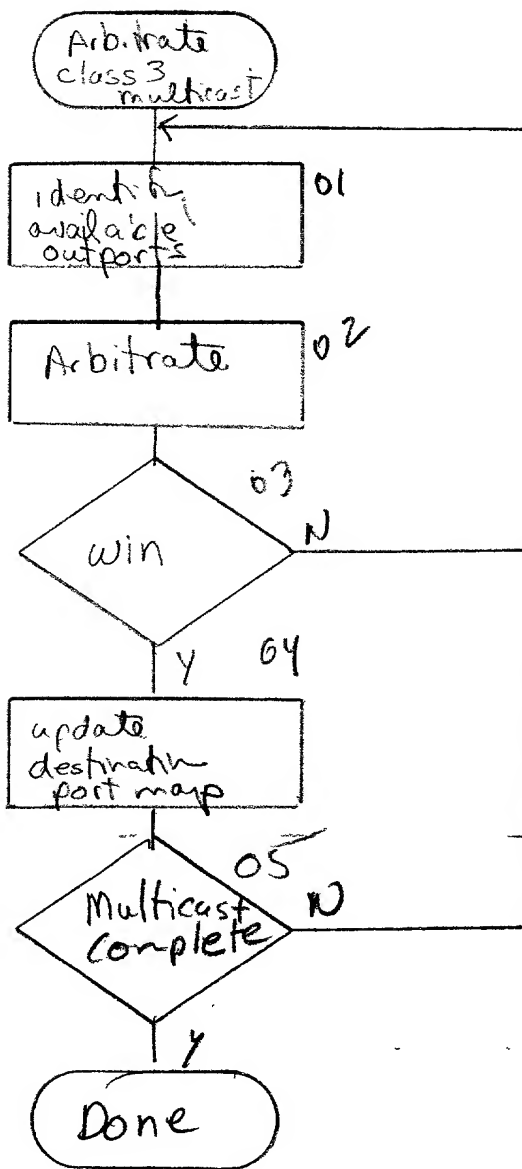


Figure 20